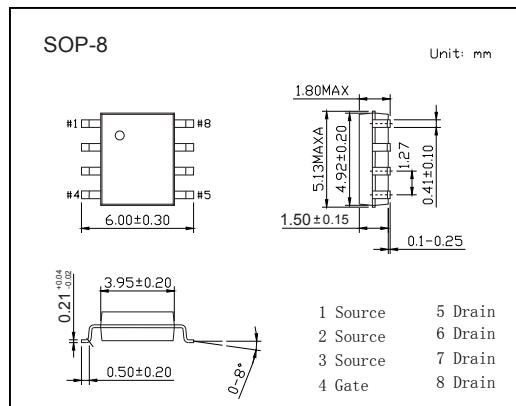
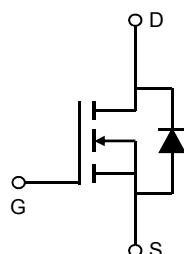


N-Channel MOSFET

■ Features

- $V_{DS} (V) = 30V$
- $I_D = 15 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 7.5m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 11.0m\Omega (V_{GS} = 4.5V)$



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	TA=25°C	I_D	15	A
	TA=70°C		12	
Pulsed Drain Current		I_{DM}	100	
Avalanche Current		I_{AS}	22	
Avalanche energy	L=0.1mH	E_{AS}	24	mJ
Power Dissipation	TA=25°C	P_D	3.1	W
	TA=70°C		2	
Thermal Resistance.Junction- to-Ambient	t ≤ 10s	R_{thJA}	40	°C/W
	Steady-State		75	
Thermal Resistance.Junction- to-Lead		R_{thJL}	24	°C
Junction Temperature		T_J	150	
Storage Temperature Range		T_{stg}	-55 to 150	

■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=250 \mu\text{A}, V_{GS}=0\text{V}$	30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30\text{V}, V_{GS}=0\text{V}$			1	μA
		$V_{DS}=30\text{V}, V_{GS}=0\text{V}, T_J=55^\circ\text{C}$			5	
Gate-Body Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.5		2.5	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10\text{V}, I_D=15\text{A}$			7.5	$\text{m}\Omega$
		$V_{GS}=10\text{V}, I_D=15\text{A}, T_J=125^\circ\text{C}$			12	
		$V_{GS}=4.5\text{V}, I_D=12\text{A}$			11	
On State Drain Current	$I_{D(on)}$	$V_{GS}=10\text{V}, V_{DS}=5\text{V}$	100			A
Forward Transconductance	g_{FS}	$V_{DS}=5\text{V}, I_D=12\text{A}$		45		S
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=15\text{V}, f=1\text{MHz}$	610		910	pF
Output Capacitance	C_{oss}		88		160	
Reverse Transfer Capacitance	C_{rss}		40		100	
Gate Resistance	R_g	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	0.8		2.4	Ω
Total Gate Charge (10V)	Q_g	$V_{GS}=10\text{V}, V_{DS}=15\text{V}, I_D=12\text{A}$	11		17	nC
Total Gate Charge (4.5V)			5		8	
Gate Source Charge	Q_{gs}		1.9		2.9	
Gate Drain Charge	Q_{gd}		1.8		4.2	
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=10\text{V}, V_{DS}=15\text{V}, R_L=1.25\Omega, R_{GEN}=3\Omega$		4.4		ns
Turn-On Rise Time	t_r			9		
Turn-Off Delay Time	$t_{d(off)}$			17		
Turn-Off Fall Time	t_f			6		
Body Diode Reverse Recovery Time	t_{rr}	$I_F= 12\text{A}, dI/dt= 500\text{A}/\mu\text{s}$	5.6		8	nC
Body Diode Reverse Recovery Charge	Q_{rr}		6.4		9.6	
Maximum Body-Diode Continuous Current	I_S				4	A
Diode Forward Voltage	V_{SD}	$I_S=1\text{A}, V_{GS}=0\text{V}$			1	V

Note : The static characteristics in Figures 1 to 6 are obtained using $<300 \mu\text{s}$ pulses, duty cycle 0.5% max.